

EASY PLC



Program and customize your internal logical sequences

- For GENSYS 2.0 and MASTER 2.0
- Inputs, outputs
- Arithmetic equations
- Logical sequences



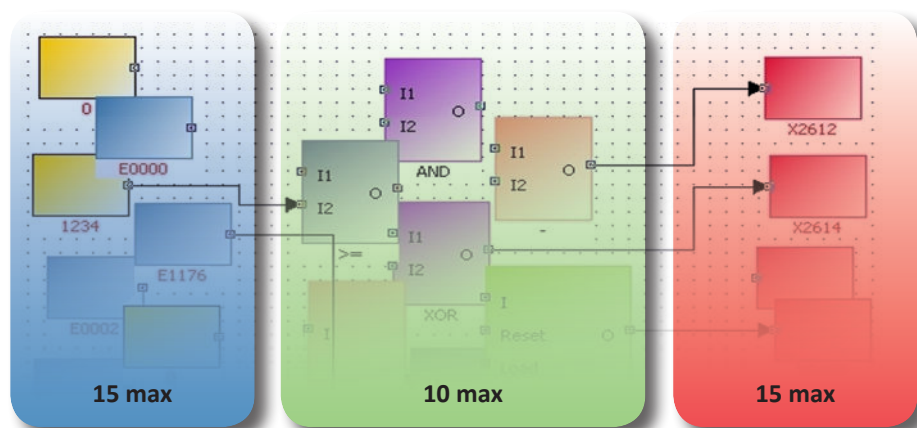
CRE's Easy PLC is a logic editor which provides graphical interface to program PLC equations. The tool is able to generate CRE propriety equations on GENSYS 2.0 family.

Easy PLC simplifies the process of writing propriety equations thanks to a user-friendly graphic environment. That includes many sheets in which the user can create inputs and outputs equations through "operators".

INPUTS AND OUTPUTS

The software contains all input / output variables divided into categories such as voltage, current or power. The user can find the required variable selecting the wanted category in the drop-down menu or typing it in the variable name field. The predefined variable list can be modified according to situations.

Each sheet manages up to 15 inputs or values, 15 outputs and 10 operators.



OPERATOR EQUATIONS

Operators must be connected to both inputs and outputs.

Each operator can be a simple or a complex equation.

They are classified into 4 categories:

LOGIC :

- AND
- NAND Not And
- OR
- XOR Exclusive OR
- NOT
- etc, ...

ARITHMETIC :

- + Plus
- - Minus
- * Multiply
- / Divide
- etc, ...

COMPARISON :

- ≠ Not equal
- ≥ Greater or Equal
- > Greater Than
- ≤ Lower or Equal
- < Lower Than

SPECIAL FUNCTIONS :

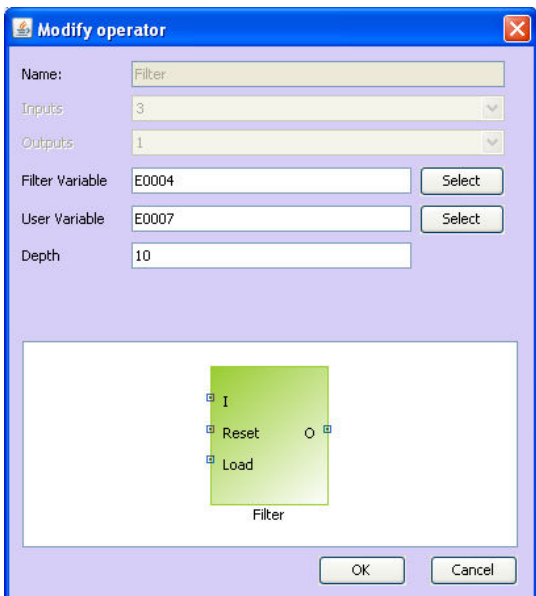
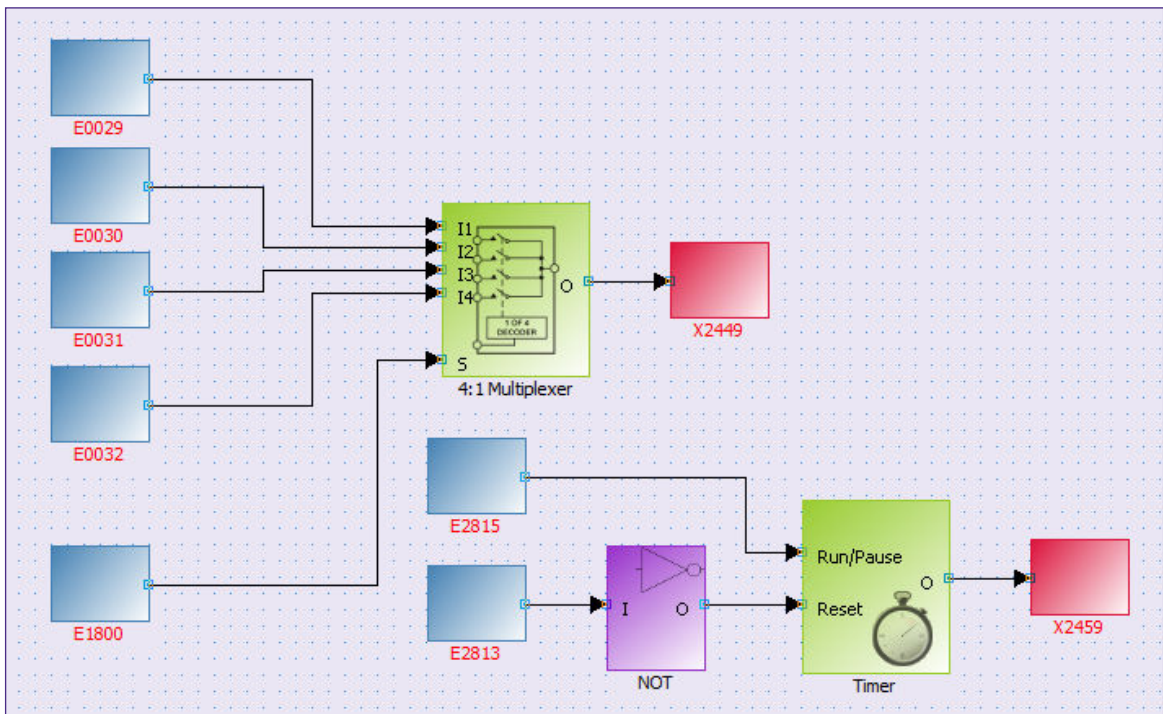
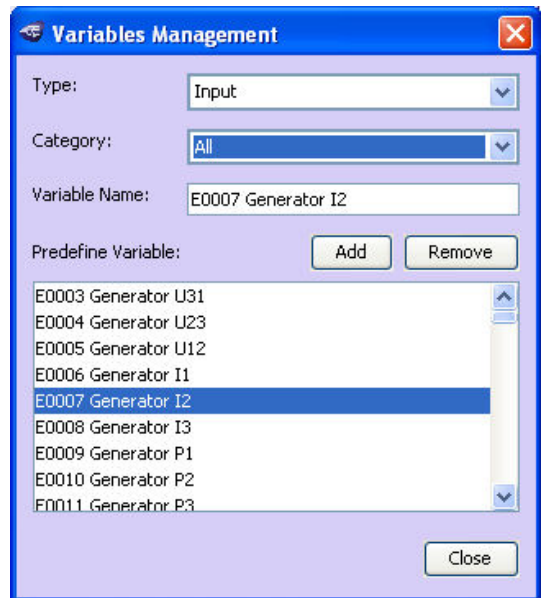
- 4:1 Multiplexer output : the value is selected between 4 input values
- Timer : Compter.
- Filter : may be used to filter noisy sensor.

Each project manages up to of 50 graphic files: users can add, rename or remove them independently.

In each file, you can create, draw objects and connect links between these objets.

You can manage a maximum of 10 operators, connected to 15 inputs and 15 outputs. Each operator can be a simple or a complex function.

The equations on sheet will only be executed when the equations match together



PART NUMBER
A70Z2

ASSOCIATED PRODUCTS
GENSYS 2.0
MASTER 2.0
GENSYS 2.0 CORE